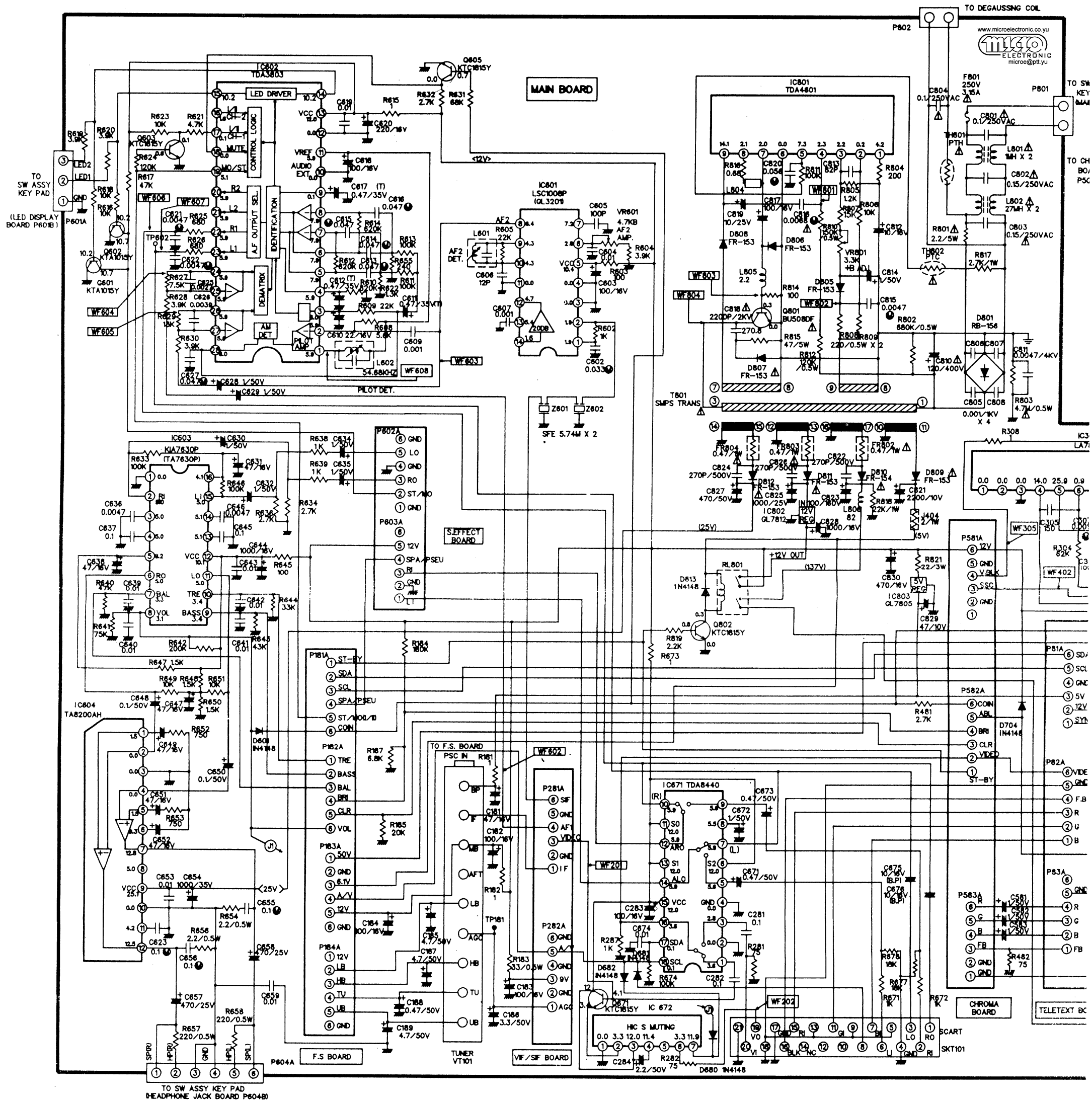


SCHEMATIC DIAGRAM (PC-08X2L, MAI



THE COMPONENTS MARKED Δ CONFORM TO VDE OR IEC GUIDELINES AND ARE ESSENTIAL FOR SAFE OPERATION OF THE SET, WHILE THOSE MARKED \triangle ARE REQUIRED FOR CORRECT OPERATION. USE SPECIFIED PARTS ONLY WHEN REPLACING.

VALUE OF RESISTOR, CAPACITOR AND INDUCTOR

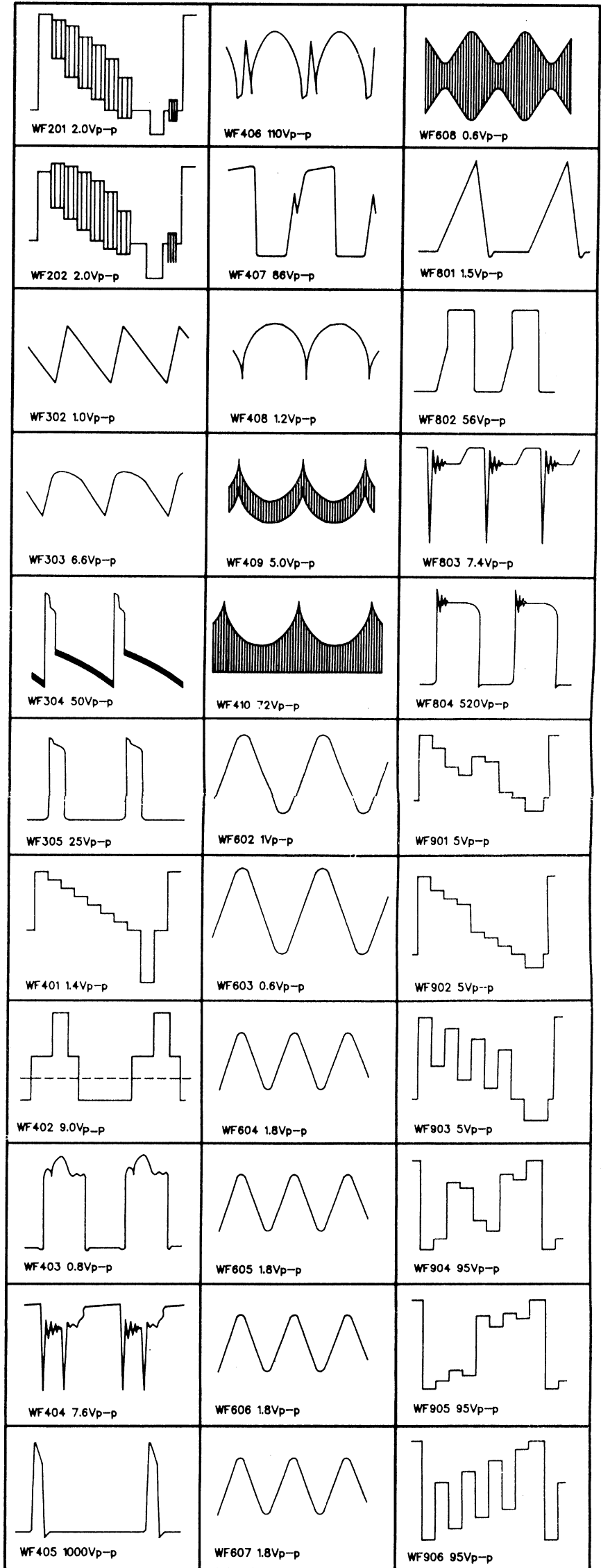
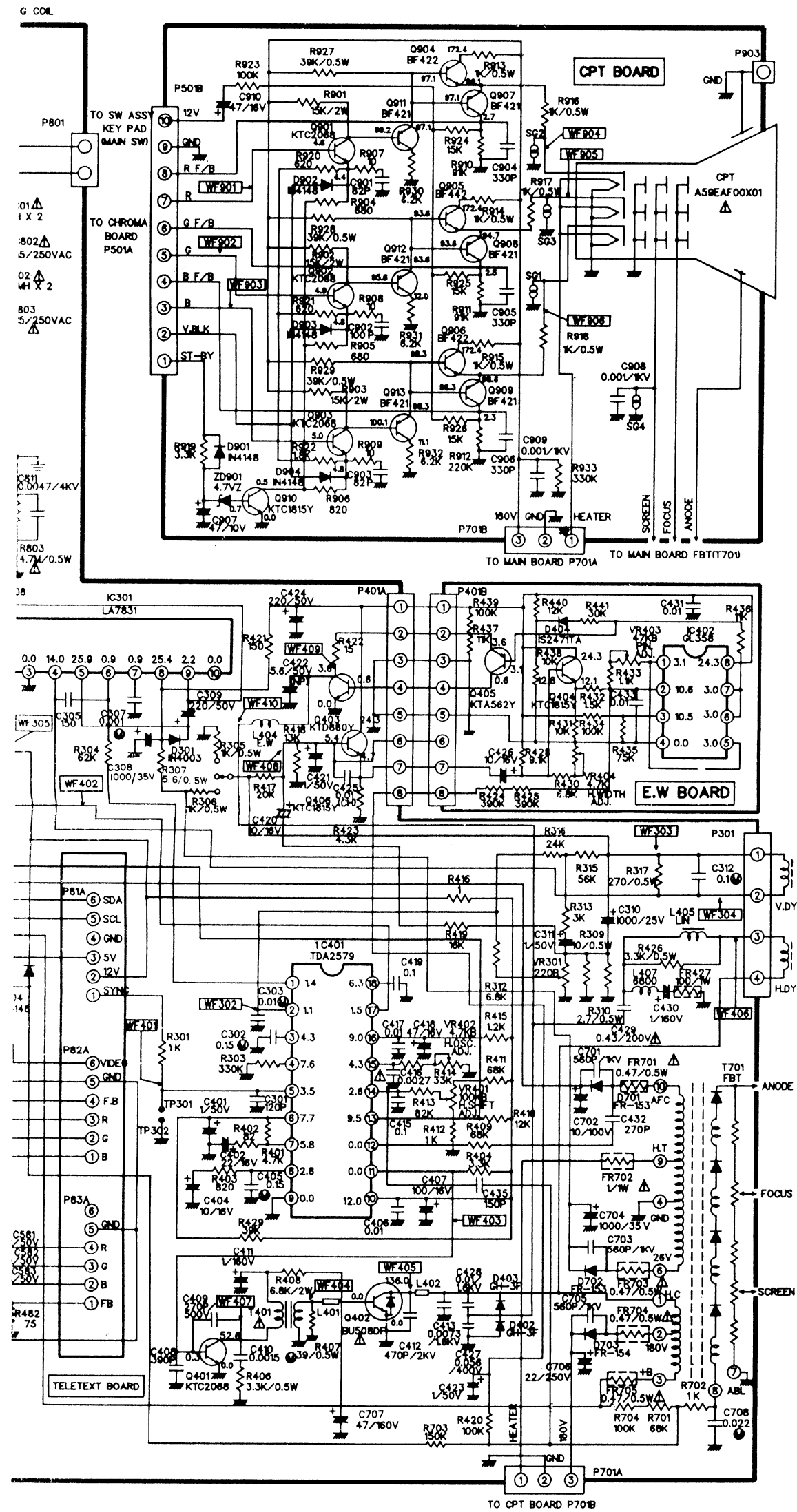
1. RESISTANCE IS IN OHM, K=1,000, M=1,000,000.
2. UNLESS OTHERWISE NOTED IN SCHEMATIC, ALL CAPACITOR VALUES ARE EXPRESSED IN μ F. THE SIGN OF M IN SCHEMATIC MEANS MYLAR CAPACITOR.
3. UNLESS OTHERWISE NOTED IN SCHEMATIC, ALL INDUCTOR VALUES ARE EXPRESSED IN μ H.

OBSERVATION OF VOLTAGES AND WAVEFORMS

1. VOLTAGES READ WITH D.V.M FROM POINT SHOWN TO CHASSIS GROUND, LINE VOLTAGE 180-270VOLTS, COLOUR BAR SIGNAL.
2. VOLTAGES READING MAY VARY $\pm 20\%$.
3. THE SCHEMATIC SHOWN IS REPRESENTATIVE ONLY.
4. ALL WAVEFORMS ARE TAKEN USING A WIDE BAND OSCILLOSCOPE AND A LOW CAPACITY PROBE.

5. CHECK FINE TUNING, BRIGHTNESS, COLOUR, AND AUDIO AT NORMAL CONDITIONS FOR BEST PICTURE, MAKE SURE THAT CONTRAST CONTROL IS IN MAXIMUM POSITION.
6. WAVEFORMS ARE TAKEN USING A STANDARD COLOUR

MAIN)



COLOUR, AND AUDIO CONTROLS
PICTURE, MAKE SURE
MAXIMUM POSITION.
STANDARD COLOUR BAR SIGNAL.

NOTICE
SINCE THIS IS A BASIC CIRCUIT DIAGRAM, THE VALUE OF
COMPONENTS AND SOME PARTIAL CONNECTION ARE SUBJECT
TO BE CHANGED FOR IMPROVEMENT.



SCHEMATIC DIAGRAM (PC-

